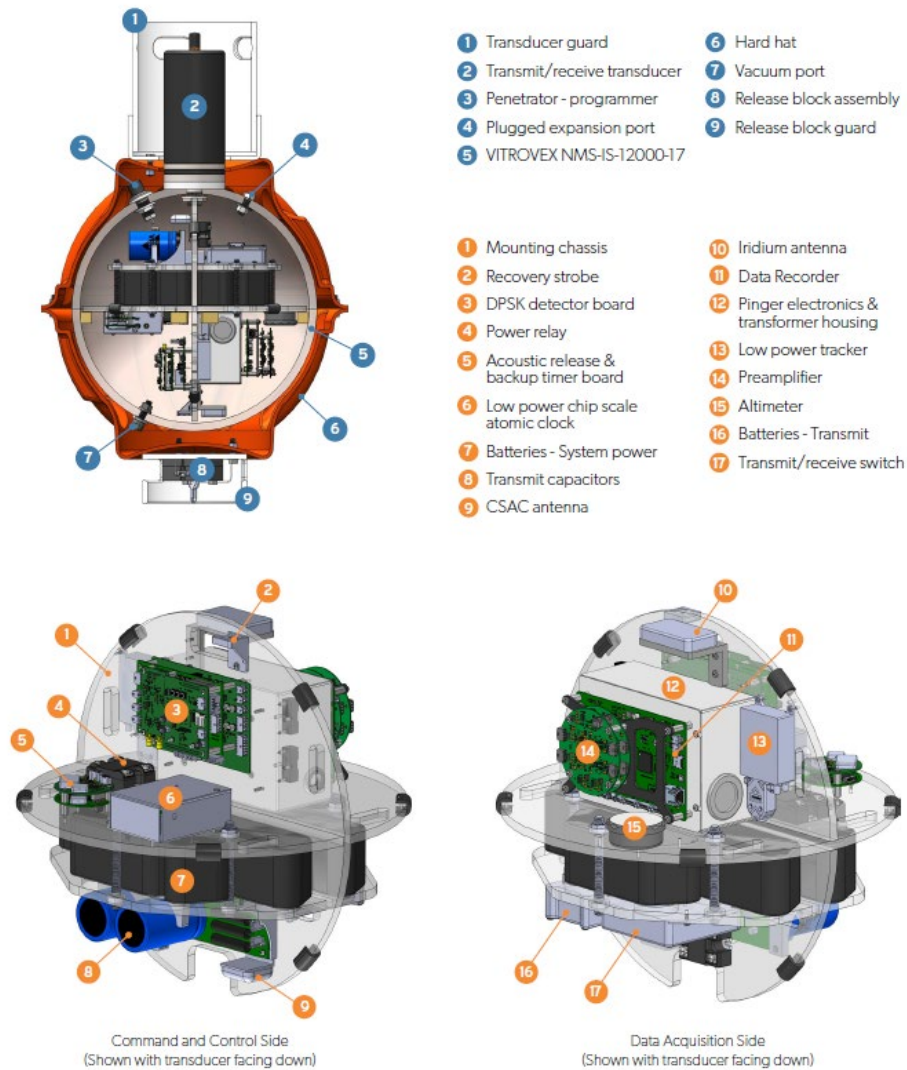


This document provides the statement of work for DBV Technology Quotation 2025-01-28 to upgrade the Chip Scale Atomic Clock (CSAC) subsystem in existing Temporary Deep Ocean Geodetic Sensors (T-DOGs) which comprise part of the Seafloor Geodesy system delivered under Princeton Purchase Order 0000356484 and is governed by the Master Purchase Agreement DBV-004691-MAS.

The work entails opening qty 4 T-DOGs to replace two sets of PCBA's. The DBV-DOGS-6000-IR-GP-DSH03-M-D12 Ver2 Upgrade shown in Figure 1 below, and the VIAVI Inc. CSAC HD Ultra Low Power OEM Board w/socket - non-RoHS.(Part Number 1005115).

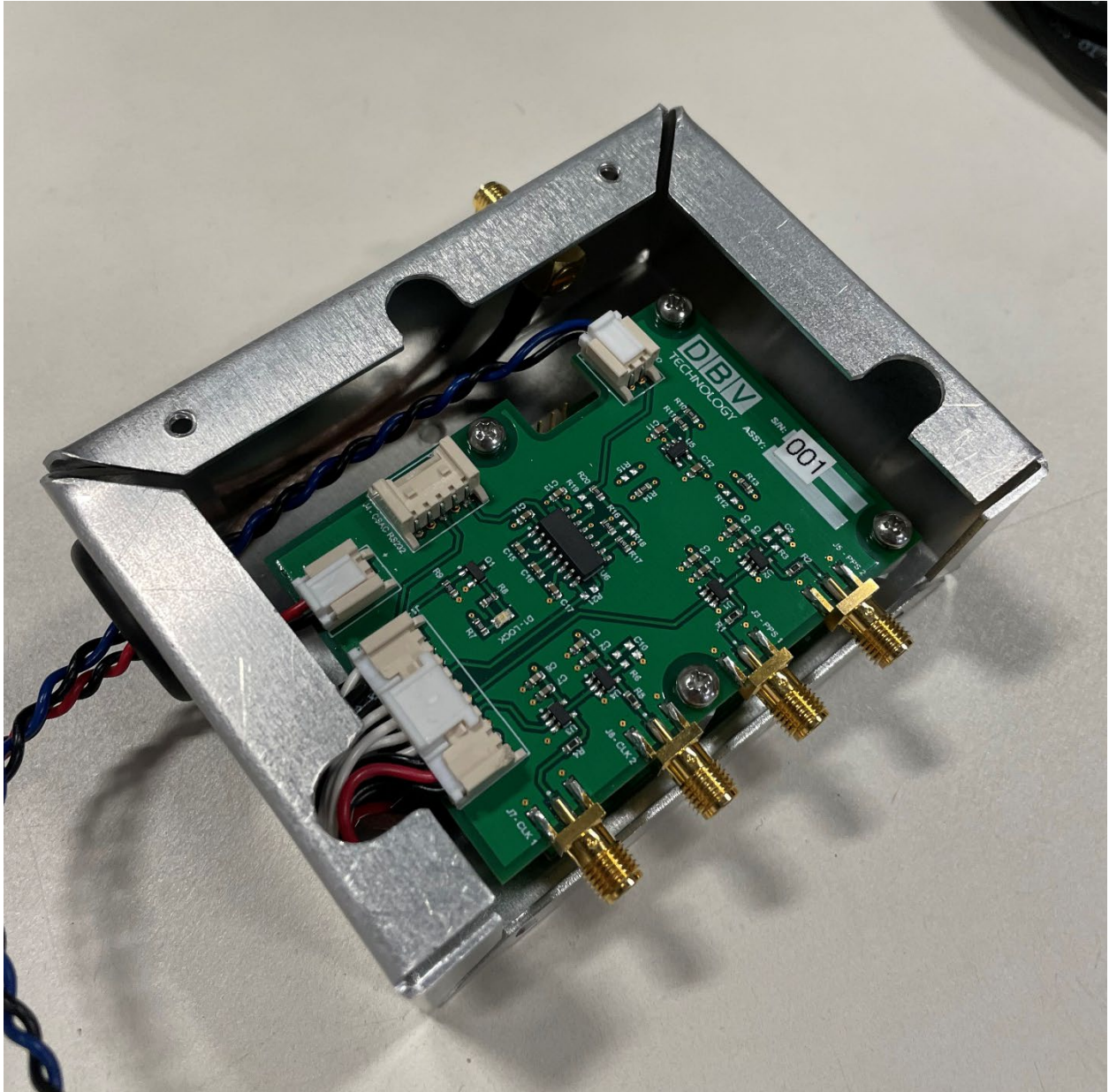
The quote for the VIAVI Inc. component not provided by DBV Technology is provided as reference.



**Figure 1** Temporary Deep Ocean Geodetic Sensor (T-DOG). Items 3 and 6 are associated with upgraded PCBA replacement.

CSAC Splitter

The CSAC Splitter PCB functions to distribute the CSAC timing signals (Clock and 1 pps) to both the Synchronized Acoustic Recorder and the Detector PCB.



### Detector Stack

The Detector PCB stack consists of three discrete PCB's connected via headers. The pre-amplifier/filter PCB (top left) and the Digital Controller PCB (top right) are hosted by the FPGA Detector PCB. The purpose of this board is to process acoustic signals (LF) in real-time to control peripherals (CSAC, SAR, Signal Generator and Power Amplifier) and to time stand the acoustic survey signals.

